

POU9-2000-0048-US1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: William F. BEAUSOLEIL et al.

Serial No.: New Application

Filed: Concurrently Herewith

For: HIGH SPEED SOFTWARE DRIVEN EMULATOR COMPRISED  
OF A PLURALITY OF EMULATION PROCESSORS WITH A  
METHOD TO ALLOW MEMORY READ/WRITES WITHOUT  
INTERRUPTING THE EMULATION

#2  
JCE836 U.S. PTO  
09/655596  
09/06/00  


INFORMATION DISCLOSURE STATEMENT

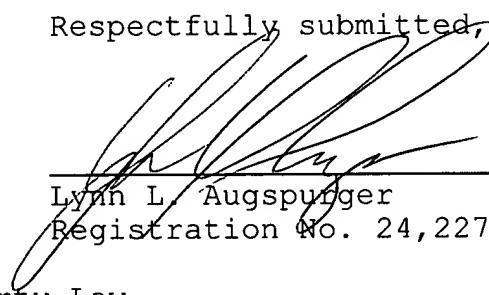
Box DD  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In compliance with applicants' duty of disclosure under 37 CFR §1.56, applicants submit the attached Form PTO-1449 listing a reference for consideration by the Examiner. A copy of the reference is submitted herewith. Kindly make this reference of record in the file of the application. The filing of this information disclosure statement shall not be construed as a representation that a search has been made or that no other art than that identified exists.

Respectfully submitted,

Date 9-6-2000

  
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